

Track 4: Green Electronics, VLSI, Communication and Sensor Based Technologies

Day 1; 12th March 2026 (Thursday)

Time of Sessions: TS-T4-S1 (9:00 AM –11:00 AM); TS-T4-S2 (12:00 Noon - 1:00 PM); TS-T4-S3 (2.00 PM-3.30 PM); TS-T4-S4 (3.30 PM-5.00 PM)

Paper ID	Paper Title	Name of the Paper Presenter with affiliation	Name and Affiliation of all Authors	Mode of the Presentation	Session Allocated
32	A Comparative study and Technical analysis of Photonic Amplifier Architectures for High-Speed Optical Systems	Dr Priya Singh, asst. Professor, Cambridge Institute of Technology, Bangalore	Dr Priya Singh, asst. Professor, Cambridge Institute of Technology, Bangalore, Dr Indu K, Cambridge Institute of Technology, Bangalore, Mr. Mallikarjuna Hampali, Cambridge Institute of Technology, Bangalore	Online	TS-T4-S1
130	Design Of Precise and Efficient BCD Adders Using Majority Gate Full Adder for Quantum Cellular Automata	Ms. Mohanapriya K (Sri Eshwar College of Engineering)	Mohanapriya K (Sri Eshwar College of Engineering), Shanmugapriya A (Sri Eshwar College of Engineering), Mohankumar M (Sri Eshwar College of Engineering), Saranyanandhini D (Sri Eshwar College of Engineering)	Online	TS-T4-S1
264	Design And Analysis of a D-Band LC Bandpass Filter and Low-Noise Amplifier for 6G Front-End Applications	Ms. Priyanka C (Dept. of Electronics and Communications Engineering, Cambridge Institute of Technology)	1. Shyam Sundar V (Research Scholar Dept of ECE Cambridge Institute of Technology) 2. Priyanka C (Dept of ECE Cambridge Institute of Technology) 3. Girish H (Dept of ECE Cambridge Institute of Technology)	Online	TS-T4-S1
288	Reliability Study of Thermal Inversion Effect on Ultra Low Power Array Multiplier Using 7nm FinFET Technology for 3D IC Integration	MD.Badhessa (ECE Department, SR University, Warangal, India)	MD. Badhessa (SR University), Shilagani Shivaprasad (SR University), J. Ajayan (SR University)	Online	TS-T4-S1
305	Design, Simulation And Performance Analysis of A-Shaped Microstrip Patch Antenna For Modern Wireless Communication Systems	Vaibhavi.K.R (Velammal College of Engineering and Technology, Madurai)	Mrs.A.Sakthi Preetha (Velammal College of Engineering and Technology, Madurai),Dr.K.Kavitha (Velammal College of Engineering and Technology, Madurai), Vaibhavi.K.R (Velammal College of Engineering and Technology, Madurai), Preethi S (Velammal College of Engineering and Technology, Madurai), Rithika S (Velammal College of Engineering and Technology, Madurai).	Online	TS-T4-S1
319	Enhancing Earthquake Prediction with Ensemble Learning: A Hybrid XGBoost and LightGBM Approach	Samaira Handa(Department of Computer Science & Engineering Chandigarh University Gharuan, Punjab, India)	Author 1 Manni Kumar (Department of Computer Science & Engineering Chandigarh University Gharuan, Punjab, India) Author 2 Nivedita Sharma (Department of Computer Science & Engineering Chandigarh University Gharuan, Punjab, India) Author 3 Mahatava Saxena (Department of Computer Science & Engineering Chandigarh University Gharuan, Punjab, India) Author 4 Samaira Handa (Department of Computer Science & Engineering Chandigarh University Gharuan, Punjab, India) Author 5 Tamanna Sarawag (Department of Computer Science & Engineering Chandigarh University Gharuan, Punjab, India) Author 6 Tamanna Kashyap (Department of Computer Science & Engineering Chandigarh University Gharuan, Punjab, India)	Online	TS-T4-S1
693	Highly Efficient Q-Band Power Amplifier Using Stacked and Doherty Topologies	V Abhiram (NITK Surathkal)	V Abhiram (NITK Surathkal), Khushi Choudhary (NITK Surathkal), Mandeep Singh (NITK Surathkal)	Online	TS-T4-S1
715	Enhanced Transformerless Full-Bridge PV Inverter with Optimized Switch Placement for Common-Mode Leakage Current Reduction	Rohan Pradeep Shinde (PhD Student, ECE Department, SOES, MIT ADT University Pune)	1. Rohan Pradeep Shinde (ECE Department, SOES, MIT Art, Design and Technology University Pune), 2. Dr. Manoj R. Hans (ECE Department, SOES, MIT Art, Design and Technology University Pune), 3. Dr. Virendra V. Shete (Dean NEP, Ranking & Accreditation, MIT Art, Design and Technology Pune), 4. Amitkumar Patil (ECE Department, SOES, MIT Art, Design and Technology Pune).	Online	TS-T4-S1

731	Duck Swarm Optimization-Enhanced Contextual Graph Attention Networks for Power-Optimized 10T2C Capacitive SRAM Near-Memory Computing Macros	SUDHA P	Sudha P(NIT,Trichirappalli) ,G.LakshmiNarayanan NIT,Trichirappalli)	Online	TS-T4-S1
735	Performance Analysis of a Race Reduced Data Flip Flop under Wide Thermal Variations for Future High-Speed Counters and Prescalers	Shilagani Shivaprasad, Department of Electronics & Communication Engineering, SR University, Warangal, India	Shilagani Shivaprasad (Dept. of ECE, SR University), Daddanala Nagavarun (Dept. of ECE, SR University), Janamanchi Rathna Koushal (Dept. of ECE, SR University), J Ajayan (Dept. of ECE, SR University), Ponnaboina Charan Sai (Dept. of ECE, SR University), MD Badhesha (Dept. of ECE, SR University)	Online	TS-T4-S1
817	COMPARITIVE ANALYSIS ON COLD CMOS IN ADVANCED FINFET TECHNOLOGY AT 300K AND 77K	Ms. Mohanapriya K (Sri Eshwar College of Engineering)	Mohanapriya K (Sri Eshwar College of Engineering), Shanmugapriya A (Sri Eshwar College of Engineering), Mohankumar M (Sri Eshwar College of Engineering), Saranyanandhini D (Sri Eshwar College of Engineering)	Online	TS-T4-S1
989	FPGA Implementation and Performance Analysis of 64-Bit Multiplier Architectures	Thivagar Thiyagarajan (NIT Trichy)	Thivagar Thiyagarajan (National Institute of Technology, Tiruchirappalli), Vaishnavi P (SASTRA Deemed University SRC Kumbaakonam) , Bhaskar Manickam (National Institute of Technology, Tiruchirappalli)	Online	TS-T4-S1
3053	Wearable and Flexible Sensors for Healthcare and Biomedical Applications	S. Lourdu Jame (Department of EEE, SRM Institute of Science and Technology, Kattankulathur, Chengalpattu dist, Tamilnadu, India)	S. Lourdu Jame (Department of EEE, SRM Institute of Science and Technology, Kattankulathur, Chengalpattu dist, Tamilnadu, India), Geetha (Department of CSE, Sreyas Institute of Engineering and Technology, Hyderabad, Telangana, India), Senthilkumar Aandi (Asst. Professor, Dept. of ECE, Chennai Institute of Technology, Chennai, TN, India), Dr. L. Bhagyalakshmi (Professor and Head, Dept. of ECE, Rajalakshmi Engineering College, Chennai, TN, India), D. Sravanthi (Dept. of CSE-DS, MLR Institute of Technology, Hyderabad, Telangana, India), Sanjay Kumar Suman (Professor and Head, Dept. of CSE – Cyber Security, Sri Shanmugha College of Engineering and Technology, Sankari, Salem, TN, India)	Online	TS-T4-S2
1148	Comparative Power Analysis of Adder Circuits for Low-Power Digital Design	Gitika (IGDTUW New Delhi)	Gitika (IGDTUW New Delhi), Prof. Greeshma Arya (IGDTUW New Delhi)	Online	TS-T4-S2
1173	Efficient Design of a Flexible Compact Antenna for Safe sub-6GHz 5G Wearable Communication Systems	Aarthi S , Annie Evangeline S	Author 1 (Aarthi S) , Author 2 (Annie Evangeline S)	Online	TS-T4-S2
1210	Design of a Flexible Leather-based Modified Rectangular Patch Antenna for Body Area Network (BAN) Application	Dwani.K,Gayathri.R	Dwani.K , Gayathri.R,	Online	TS-T4-S2
1217	Surface Acoustic Wave based Photonic Gas Sensor using Polyvinyl Alcohol film	Gaddameedi Vinay (NITK)	Gaddameedi Vinay(NITK),Pragnya Yanguntekar (Central University of Karnataka),Mandeep Singh(NITK)	Online	TS-T4-S2
1233	A Classical–Quantum Framework for Quantum Secure Direct Communication	Bhanu Pratap (Student Dept. of ECE NITK Surathkal Mangalore India)	Author 1 Bhanu Pratap (Student, Dept. of ECE NITK Surathkal Mangalore India) Author 2 Mandeep Singh (Associate Professor, Dept. of ECE NITK Surathkal Mangalore India)	Online	TS-T4-S2
1322	First-Principles Modeling of H2S Adsorption on Silicon Carbide Surfaces	Sitha V R (NITK)	Sitha V R (NITK), Sahana C (NITK), Dr. Mandeep Singh (NITK)	Online	TS-T4-S3
1324	AALIS: An Automated Arch Linux Installation System	Aarshita Acharya(University of Houston, Houston, USA)	Stuti Shah(Adani University Ahmedabad, India),Aarshita Acharya(University of Houston Houston, USA),Jignesh Thaker(Adani University Ahmedabad, India)	Online	TS-T4-S3
1358	Implementation of Smart Doorbell for Hearing Impaired using FPGA,ModelSim and MATLAB	Chejarla Venkata Surya Sai Anish(Amrita Vishwa Vidyapeetham, Amritapuri)	Chejarla Venkata Surya Sai Anish(AV Amritapuri)	Online	TS-T4-S3
1375	Single Event Upset Characterization in the Control Path of RISC-V Processor Core	Shreyas R (International Institute of Information Technology Bangalore)	Shreyas R (MS By Research Student) Subir Kumar Roy (Professor)	Online	TS-T4-S3
1399	Transposable SRAM Architecture Enabling Bidirectional Access for Symmetric STDP Learning in Neuromorphic Systems	Grishma Bhawsar (Nirma University Ahmedabad)	Author 1 GRISHMA BHAWSAR (NIRMA UNIVERSITY AHMEDABAD), Author 2 Dr. ANKIT SHARMA (NIRMA UNIVERSITY AHMEDABAD)	Online	TS-T4-S3

1489	Power and Area Optimized FFT Architecture Through Multiplier-Level Hardware Design	Ezhilkrithik M (Amrita School of Engineering, Coimbatore)	Ezhilkrithik M (Amrita School of Engineering, Coimbatore), Sujith Krishna P G (Amrita School of Engineering, Coimbatore), Uday Venu Gopal B (Amrita School of Engineering, Coimbatore), Ramesh S R (Amrita School of Engineering, Coimbatore)	Online	TS-T4-S3
1518	Performance Analysis of a 50 Gbit/s PAM-4 Free-Space Optics Link Under Realistic Weather Conditions	Riya (Chandigarh University, Mohali, India)	Mehtab Singh (Chandigarh University, Mohali, India), Naveen Kumar (Bhai Gurdas Institute of Engineering and Technology Sangrur, Punjab), Rupinder Kaur (CGC University, Mohali, India), Somia A. Abd El-Mottaleb (Alexandria Higher Institute of Engineering and Technology Alexandria, Egypt), Riya (Chandigarh University, Mohali, India), Amit Grover (Shaheed Bhagat Singh State University, Ferozepur),	Online	TS-T4-S3
1599	SPM-Arrow: A Memory-Hierarchy Optimized RISC-V Vector Accelerator for Energy-Efficient Machine Learning	Arun Kirthick (AMRITA VISHWA VIDYAPEETHAM,AMRITAPURI)	kisor s (AMRITA VISHWA VIDYAPEETHAM,AMRITAPURI), Arun Kirthick (AMRITA VISHWA VIDYAPEETHAM,AMRITAPURI), Rajesh Kannan Megalingam (AMRITA VISHWA VIDYAPEETHAM,AMRITAPURI)	Online	TS-T4-S4
3072	AI-Driven Fault Detection and Predictive Maintenance in Green Electronic Systems	Charul Jain (School of Interdisciplinary Sciences Symbiosis University of Applied Sciences Indore,India)	Charul Jain (School of Interdisciplinary Sciences Symbiosis University of Applied Sciences Indore, India), Bhupinder kaur (Department of ECE, UIE, Chandigarh university, Mohali,India), Sumit Kumar (Department of Computer science and engineering, Chandigarh Engineering College,CGC Mohali, India), Nodirjon Otabayev (Department of Vehicle Engineering Fergana State Technical University, Fergana, Uzbekistan)	Online	TS-T4-S4
1883	Dispersion and Bandgap Analysis of Miniaturized Meander-Lined EBG for Sub-GHz Communication	Crasencia Mary A, Divya Lakshmi AP	Author 1 (Crasencia Mary A), Author 2 (Divya Lakshmi AP)	Online	TS-T4-S4
1892	Under Water Object detection using Deep Learning.	K PRANAV KUMAR(VCE HYDERABAD)	K PRANAV KUMAR (VCE HYDERABAD),G.A.E. Satish Kumar (VCE HYDERABAD), K Pranathi (VCE HYDERABAD),H SAI CHARAN(VCE HYDERABAD)	Online	TS-T4-S4
1958	Internet of Things for Precision Agriculture: Advanced Solutions for Crop Prediction, Monitoring and Security Systems	Dr.Swapna P S (Mar Baselios College of Engineering and Technology, Trivandrum, Kerala)	Mr.Arun M (Mar Baselios College of Engineering and Technology) Dr.Swapna P S (Mar Baselios College of Engineering and Technology)	Online	TS-T4-S4
2034	Proactive Make-Before-Break Handover Using Signal Degradation Prediction for Robust Urban Mobile Sensing Systems	Mrs.R.Tharaniya (Research Scholar, Thiagarajar College of Engineering, Madurai)	Tharaniya R (Research Scholar, Thiagarajar College of Engineering, Madurai), D.Kiruthika (Research Scholar, Thiagarajar College of Engineering, Madurai), Rajeshkumar J (Assistant Professor, REVA University, Bangalore), Joy Christina J (Student, Thiagarajar College of Engineering, Madurai), Janardan M (Student, Thiagarajar College of Engineering, Madurai), Ananthi G (Associate Professor, Thiagarajar College of Engineering, Madurai).	Online	TS-T4-S4
2060	A Lightweight Ensemble Framework for Gate-Level Hardware Trojan Detection	Maganti Shanmukha Sri Datta (Department of Electronics and Communication Engineering Amrita School of Engineering, Coimbatore Amrita Vishwa Vidyapeetham, India)	Maganti Shanmukha Sri Datta Department of Electronics and Communication Engineering Amrita School of Engineering, Coimbatore Amrita Vishwa Vidyapeetham, India Ramesh S R Department of Electronics and Communication Engineering Amrita School of Engineering, Coimbatore Amrita Vishwa Vidyapeetham, India	Online	TS-T4-S4
Day 2; 13th March 2026 (Friday)					
Time of Sessions: TS-T4-S5 (9:00 AM –10:30 AM); TS-T4-S6 (2:30 PM - 4:00 PM); TS-T4-S7 (5:00 PM –6:30 PM)					
3703	Novel Dual-band Design of MIMO Configuration for 5G/6G Applications	Soumyajit Pat (VIT Vellore)	Hridhhi Biswas (VIT Vellore), Soumyajit Pat (VIT Vellore), Anish Chattaraj (VIT Vellore), Dilip Kumar Choudhary (VIT Vellore)	Offline	TS-T4-S5
4024	A Low-Power PMOS-Input OTA with Integrated Buffer for Biomedical Electrogram Signal Acquisition	Ankit Kumar (ABV-IIITM Gwalior)	Ankit Kumar (ABV IIITM Gwalior), Anoop Garg (ABV IIITM Gwalior), Hrishikesh Das (ABV IIITM Gwalior), Gaurav Kaushal (ABV IIITM Gwalior), Manisha Pattanaik (ABV IIITM Gwalior)	Offline	TS-T4-S5
4025	Comparative Analysis of Sample-and-Hold Circuits for Low-Voltage and Low-Frequency Biomedical Applications	ANOOP GARG(ABV-IIITM Gwalior)	ANOOP GARG(ABV-IIITM GWALIOR), ANKIT KUMAR(ABV-IIITM GWALIOR), Hrishkesh DAS(ABV-IIITM Gwalior), Gaurav Kaushal(ABV-IIITM Gwalior), Manisha Pattanaik (ABV-IIITM Gwalior)	Offline	TS-T4-S5

4650	Performance Analysis of Cooperative VLC Systems Using OOK and L-PPM Modulation Techniques with Varying FOV and Responsivity Parameters	Dr. Vipul Dixit (Bennett University Greater Noida)	Prashant Dwivedy (PDEU Gandhinagar), Vipul Dixit (Bennett University Greater Noida), Atul Kumar (MANIT Bhopal)	Offline	TS-T4-S5
4048	Effect of Random Dopant Fluctuation (RDF) on Performance of Homo- and Hetero-Tunnel FETs	Dr. Ashish Maurya (Bennett University, India)	Ashish Maurya (Bennett University), Abhay Pratap (MANIT Bhopal, Shailendra Yadav (IIT Roorkee), Satish Chandra (MNNIT Allahabad), Sagar (GSV Gujarat), Amrita Joshi (NTU Singapore)	Offline	TS-T4-S5
2085	Fast & Power Efficient Voltage Translators for IO/Memory Sub-Systems	Mainak Ghosh (7Rays Semiconductors Pvt Ltd)	Mainak Ghosh (7Rays Semiconductors), Poleboyina Manideep(7Rays Semiconductors),Viraj Panchal(7Rays Semiconductors), Priyanka Nirakari(7Rays Semiconductors), Ramesh Karmungi(7Rays Semiconductors)	Online	TS-T4-S6
2135	Design and Evaluation of a Millimeter-Wave MIMO Antenna at 39 GHz for On-Body 5G (n260) Band	Mr. Devraj Gautam (Jamia Millia Islamia, New Delhi)	Devraj Gautam Department of Electrical Engineering Jamia Millia Islamia New Delhi, India. devrajgautam10@gmail.com, Abdul Quaiyum Ansari Department of Electrical Engineering Jamia Millia Islamia New Delhi, India. aqansari@jmi.ac.in Binod Kumar Kanaujia Department of Electronics & Communication Engineering Dr. B. R. Ambedkar NIT Jalandhar, India. bkanaujia@ieee.org	Online	TS-T4-S6
2228	Adaptive TCP Congestion Control using Deep Reinforcement Learning for TCP CUBIC Parameter Optimization	Varun Sharma (NSUT, New Delhi)	Varun Sharma (NSUT, New Delhi), Madhur Bakshi (NSUT, New Delhi), Deepanshu Sharma (NSUT, New Delhi), Vivek Mehta (NSUT, New Delhi)	Online	TS-T4-S6
2277	Hybrid and Gradient-Boosted Regression Models for Accurate Transistor Drain Current Prediction	M.Pravallika, Aditya Institute of Technology and Management	P.SIRISH KUMAR (Aditya Institute of Technology and Management),A.Jayalaxmi (Aditya Institute of Technology and Management), Ch.Rajasekhar (Aditya University), K.Kavitha (, GMR Institute of Technology and Management), Ramya Krishna Isukapalli (SRKR Engineering College), M.Pravallika (Aditya Institute of Technology and Management)	Online	TS-T4-S6
2351	Design of Power-Optimized Approximate Arithmetic Logic Unit	Chethana K A (Amrita Vishwa Vidyapeetham)	Chethana K A (ASE Bengaluru), Kamatchi S (ASE Bengaluru), C Paramasivam (ASE Bengaluru)	Online	TS-T4-S6
2390	Design and Performance Evaluation of a Low-Power, Highly Sensitive Double-Gate Junctionless FET UV Photodetector	Devendra Rani (PDPM IIITDM Jabalpur)	Devendra Rani (PDPM IIITDM Jabalpur), Pushpa Raikwal (PDPM IIITDM Jabalpur), Koushik Dutta (PDPM IIITDM Jabalpur)	Online	TS-T4-S6
2710	Nanoscale Dynamic Voltage Comparators: A Thermal Reliability Study	Ms. SHARVANI YEDULAPURAM (SR UNIVERSITY)	Ms. SHARVANI YEDULAPURAM (SR UNIVERSITY), Dr. J. Ajayan (SR University) and Dr. L. M. I. Leo Joseph (SR University)	Online	TS-T4-S6
2763	FPGA-Based Multi-Function Digital Waveform Generator using Pmod DA4	Mr. Sathyanarayanan P. V (Amrita School of Engineering, Coimbatore)	1. Hemathri Goutham Raj R (Amrita School of Engineering, Coimbatore), 2. Rohith Pranay P (Amrita School of Engineering, Coimbatore), 3. Sathyanarayanan P. V (Amrita School of Engineering, Coimbatore), 4. Prabhu E (Amrita School of Engineering, Coimbatore)	Online	TS-T4-S6
2774	DESIGN OF AN ON-CHIP MICRORING RESONATOR BASED SILICON PHOTONIC SENSOR	BANDI SRI DURGA PRASAD (KALASALINGAM ACADEMY OF RESEARCH AND EDUCATION)	Jenila C (Anna University), Aarthi V.P.M.B (Kalasalingam Academy of Research and Education), Vasipalli Sai Kiriti (Kalasalingam Academy of Research and Education), Bandi Sri Durga Prasad (Kalasalingam Academy of Research and Education), Yampalaku Ganesh (Kalasalingam Academy of Research and Education)	Online	TS-T4-S7
2862	Bio-RV: Low-Power Resource-Efficient RISC-V Processor for Biomedical Applications	Vijay Pratap Sharma (IIT Indore)	Author 1: Vijay Pratap Sharma (IIT Indore) Author 2: Annu Kumar (IIT Indore) Author 3: Mohd Faisal Khan (IIT Indore) Author 4: Mukul Lokhande (IIT Indore) Author 5: Santosh Kumar Vishvakarma (IIT Indore)	Online	TS-T4-S7
3205	On-Chip Hydrogen Sulfide Detection Using Silicon Carbide Hybrid Plasmonic Slot Waveguide	Sahana C (National Institute of Technology Karnataka, Surathkal, India)	Sahana C (National Institute of Technology Karnataka, Surathkal, India), Sitha V R (National Institute of Technology Karnataka, Surathkal, India), Umesh Tiwari (CSIR-Central Scientific Instruments Organisation, India), Mandeep Singh (National Institute of Technology Karnataka, Surathkal, India)	Online	TS-T4-S7
3461	A Fully Pipelined Reconfigurable Max-Average Pooling Architecture for FPGA-Based CNN Accelerators	Dr. Siva Ramakrishna P (VIT-AP University)	Butchi Babu Sarnala (VIT-AP University), Dr. Siva Ramakrishna P (VIT-AP University)	Online	TS-T4-S7

3513	Comparative Analysis of Passive Noise Cancellation Using LMS, NLMS, RLS, and APA Algorithms	Anupam Krishna	Author 1 :Anupam Krishna (B-Tech Student) Author 2: Abhiram Rajesh (B-Tech Student) Author 3: Abin O K (B-Tech Student) Author 4: Amal Vinod (B-Tech Student) Author 5 Akshay Gopal (B-Tech Student) Author 6 Karthik S Unnithan (B-Tech Student) Author 7:Anoop S Asst. Professor	Online	TS-T4-S7
3537	Design and Development of Venn Diagram Shaped Microstrip Patch Antenna For Female Breast Tumor Detection	Hemant Kumar(MITS-DU Gwalior)	Hemant Kumar (MITS-DU Gwalior), Dr. Vandana Vikas Thakare (MITS-DU Gwalior)	Online	TS-T4-S7
3605	Modeling and Simulation of a MOSFET Biosensor for Early stage Oral Cancer Detection	Shaik Sufiya Khatun (Kalasalingam Academy of Research and Education)	Abha Dargar (Kalasalingam Academy of Research and Education), Shashi Kant Dargar (Kalasalingam Academy of Research and Education), Zayd Abdul Bhari (Kalasalingam Academy of Research and Education), Shaik Sufiya Khatun (Kalasalingam Academy of Research and Education), Vijeyandar S (Kalasalingam Academy of Research and Education)	Online	TS-T4-S7
3607	Improving WiFi Fingerprint Localization Using an Optimized FasterKAN Architecture	Venkata Siva Karthik Vurubindi (B. Tech, CSE, SRM University AP), Aditya Varma Chandika (B. Tech, CSE, SRM University AP), Siva Mandalapu (B. Tech, CSE, SRM University AP)	Dr. Ajay Bhardwaj (SRM University AP), Venkata Siva Karthik Vurubindi (SRM University AP), Aditya Varma Chandika (SRM University AP), Siva Mandalapu (SRM University AP), Hasini Kaltepalli (SRM University AP)	Online	TS-T4-S7
Day 3; 14th March 2026 (Saturday)					
Time of Sessions: TS-T4-S8/S9/S10 (9:30 AM - 11:00 AM)					
3621	A Synthesis-Level Activity-Aware Optimization Technique for Low-Power Content Addressable Memory Design	Prajjwal Tripathi (MANIT,Bhopal)	Prajjwal Tripathi (MANIT, Bhopal), Suryakant Gautam (MANIT, Bhopal)	Online	TS-T4-S8
3623	Design and Machine Learning-Based Optimization of a 1 × 2 Hexagonal Microstrip Patch Antenna for Sub-6 GHz 5G Applications	Ayush Ranjan Sinha (DTU)	M. Ganesh (DTU), Ayush Ranjan Sinha (DTU), Chirag Prabhat (DTU), Devansh Verma (DTU)	Online	TS-T4-S8
3712	Deep Reinforcement and Federated Learning-Based Joint Spectrum and Power Allocation in D2D-Enabled Cellular Networks	Kolakani Hansika Bose (SRM University AP)	Dr. Ajay Bhardwaj (SRM University AP), Srinivasarao Yarlagadda (SRM University AP), Telaprolu Dhanushya Sri (SRM University AP), Kolakani Hansika Bose (SRM University AP), Lebaka Kyathi (SRM University AP), Thati Jaya Sarayu (SRM University AP)	Online	TS-T4-S8
2582	AI-Driven Cross-Layer Optimization of Hybrid Optical-Wireless 5G Transport Systems	Praveen Kumar Pal (Nokia San Jose, CA, USA)	Praveen Kumar Pal (Nokia San Jose, CA USA)	Online	TS-T4-S8
1524	Design and Analysis of a Quad-Band Microstrip Array Antenna for V2X and Autonomous Vehicle Applicati	Zubayer Bin Rahman (Ahsanullah University of Science and Technology)	Rudro Saha(AUST), Zubayer Bin Rahman(AUST), Irtiza Radif Rahman(AUST), A.K. M. Ehtesanul Islam(AUST)	Online	TS-T4-S8
1573	Exploring Dual-Band MIMO Antenna Performance at 9 GHz and 19 GHz for Next-Generation Autonomous Vehi	Rudro Saha(Ahsanullah University of Science and Technology)	Rudro(AUST),Zubayer(AUST),Musfiqul(AUST),Ayon(AUST), Radif(AUST),A.K.M. Ehtesanul Islam(AUST)	Online	TS-T4-S8
134	Optimization Of Energy Efficient Task Offloading for 5G IoT Enable Edge Computing for IIoT using Hybrid Swarm Intelligence Algorithm	NA	Author1 - Voore Subba Rao, Research Scholar, Department of CSE, Dr. A.P.J. Abdul Kalam Technical University, Lucknow. Author2 - G.Ravindra Babu, Supervisor, Swami Vivekanand Subharti University, Meerut,	Offline	TS-T4-S9

574	AgroRover: Autonomous Ultrasonic Pest Repellent and Smart Irrigation Rover for Sustainable Farming	Riddhiraj Chanda, Ritam Das, Department of ECE IEM, UEM Kolkata Kolkata, India	Riddhiraj Chanda Department of ECE IEM, UEM Kolkata Kolkata, India Sandip Bhattacharya Senior IEEE Member Department of ECE SR University, Warangal, Telangana India - 506371 Ritam Das Department of ECE IEM, UEM Kolkata Kolkata, India Monodip Roy Department of ECE IEM, UEM Kolkata Kolkata, India Md. Ayan Akhtar Khan Department of ECE IEM, UEM Kolkata Kolkata, India Suchismita Chatterjee Department of ECE IEM, UEM Kolkata Kolkata, India	Offline	TS-T4-S9
1169	ImmuneMesh: A Hybrid BIO-Inspired Cyber Defense System	Shivangi Chauhan, Shoolini University, Solan, Himachal Pradesh	Author 1: Shivangi Chauhan (Shoolini University, Solan, India), Author 2: Dr. Arvind Sharma (Shoolini University, Solan, India), Author 3: Gousia Habib (University of Helsinki, Finland)	Offline	TS-T4-S9
2070	Miniaturized Wide Band Dual L-Slot On-chip Antenna with Enhanced Gain for 5G Applications	Vipin Singh (AVVP Coimbatore)	Author 1 Vipin Singh (AVVP Coimbatore) Author 2 Dr. Harshavardhan Singh (AVVP Coimbatore)	Offline	TS-T4-S9
2071	Mitigation of Crosstalk of On-chip Antenna in RFIC using Dual Guard Band Method	Vipin Singh (AVVP Coimbatore)	Author 1 Vipin Singh (AVVP Coimbatore) Author 2 Dr. Harshavardhan Singh (AVVP Coimbatore)	Offline	TS-T4-S9
2499	Design and Analysis of a Stack-Enhanced LECTOR-Based Power Gating Scheme with PMOS Sleep Transistors	Mahesh Arvind Bidarkote VIT University Bhopal	Mahesh Bidarkote(VIT University Bhopal, India), M. Suresh(VIT University Bhopal, India), Deep Chandra Upadhyay(VIT University Bhopal, India), Ankur Beohar(VIT University Bhopal, India), and Pallabi Sarkar (VIT University Bhopal, India)	Offline	TS-T4-S9
2551	Highly Reliable and Enhanced Soft-Error-Tolerance Read-Decoupled Radiation-Hardened SRAM cell	Dr. Rachana Ahirwar (PDPM-IIITDM Jabalpur)	Rachana Ahirwar (PDPM-IIITDM Jabalpur), Dip Prakash Samajdar (PDPM-IIITDM Jabalpur), Manisha Pattanaik (ABV-IIITM Gwalior), Pankaj Srivastava (ABV-IIITM Gwalior)	Offline	TS-T4-S10
3192	A Step-by-Step Tuning of Mathematical Parameters (Verilog-A) for Double Loop Behavior of RRAM Device Modeling	Sahil Kumar (PDPM IIITDM Jabalpur)	Sahil Kumar (PDPM IIITDM Jabalpur), Dr. Trivesh Kumar (PDPM IIITDM Jabalpur), Dr. Koushik Dutta (PDPM IIITDM Jabalpur)	Offline	TS-T4-S10
3378	Wideband Metamaterial Absorber based on Polylactic Acid for Stealth Technology	Dilip Kumar Choudhary (VIT Vellore)	Kushagra Pratap (VIT Vellore), Shiven Shiv Senkumar (VIT Vellore), Tanishq Kapoor (VIT Vellore), Dilip Kumar Choudhary (VIT Vellore)	Offline	TS-T4-S10
3436	Block-Sparse Joint Activity and Data Detection for Grant-Free Random Access in 6G mMTC Systems	Dr. Parikshit Kishor Singh (IIIT KOTA)	Sandeep Kumar(IIIT KOTA),Parikshit Kishor Singh(IIIT KOTA),Anukram Mishra(Smart Metering Technical Advisor),Varun Dhaundiyal(IIIT KOTA),Manjot Singh Sodhi(IIIT KOTA)	Offline	TS-T4-S10
3530	Design of a Compact Wideband Antenna Employing Spline-Driven Ground Structure for 5G Applications	Dr. Naveen Mishra (Vellore Institute of Technology, Vellore)	Udit Pratap Singh (VIT Vellore), Naveen Mishra (VIT Vellore)	Offline	TS-T4-S10
3533	Quad-Port Ultra-Wideband Circularly Polarized Self-Isolated MIMO Antenna for mm-Wave Communication	Dr. Naveen Mishra (VIT, Vellore)	Author 1. DURAI PANDURANGAN (VIT, Vellore) Author 2. NAVEEN MISHRA (VIT, Vellore)	Offline	TS-T4-S10